



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION PTO-1449				ATTY. DOCKET NO. SUN-P9325-MEG		APPLICATION NO. 10/637,169	
				APPLICANT Marc Tremblay, et al.			
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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
RF	Publication: "Speculative Synchronization: Applying Thread-Level Speculation to Explicitly Parallel Applications" by Jose F. Martinez and Josep Torrellas, Dept. of Computer Science, University of Illinois at Urbana-Champaign, Urbana, IL 61801 USA, http://iacoma.cs.uiuc.edu , XP-002285169, published in ASPLOS X, 10-2002, Pgs. 18-29.						
RF	Publication: "Speculative Lock Elision: Enabling Highly Concurrent Multithreaded Execution" by Ravi Rajwar and James R. Goodman, Computer Sciences Department, University of Wisconsin-Madison, Madison, WI 53706 USA, rajwar@cs.wisc.edu , XP-001075852, published in IEEE Journal 1-12-2001, Pgs. 294-305.						
RF	Publication: "Transactional Memory: Architectural Support for Lock-Free Data Structures" by Maurice Herlihy, Digital Equip. Corp. Cambridge Research Laboratory, Cambridge, MA 02139, herlihy@crl.dec.com and J. Eliot B. Moss, Dept. of Computer Science, University of Massachusetts, Amherst, MA 01003, moss@cs.umass.edu , XP-000380375, published in Computer Architecture News, May 21, 1993, Pgs. 289-300						
RF	Publication: "Enhancing Software Reliability with Speculative Threads" by Jeffrey Oplinger and Monica S. Lam, Computer Systems Laboratory, Stanford University, jeffop@stanford.edu , XP-002285168 published in ASPLOS X, 10-2002, Pgs. 184-196						
EXAMINER <i>Robert J. [Signature]</i>				DATE CONSIDERED 11/2/2006			

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